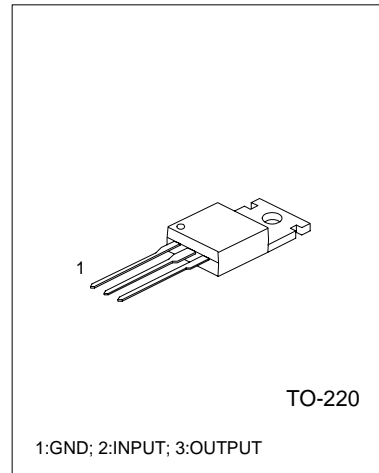


UTC79XX LINEAR INTEGRATED CIRCUIT

3 TERMINAL 1.5A NEGATIVE VOLTAGE REGULATORS

DESCRIPTION

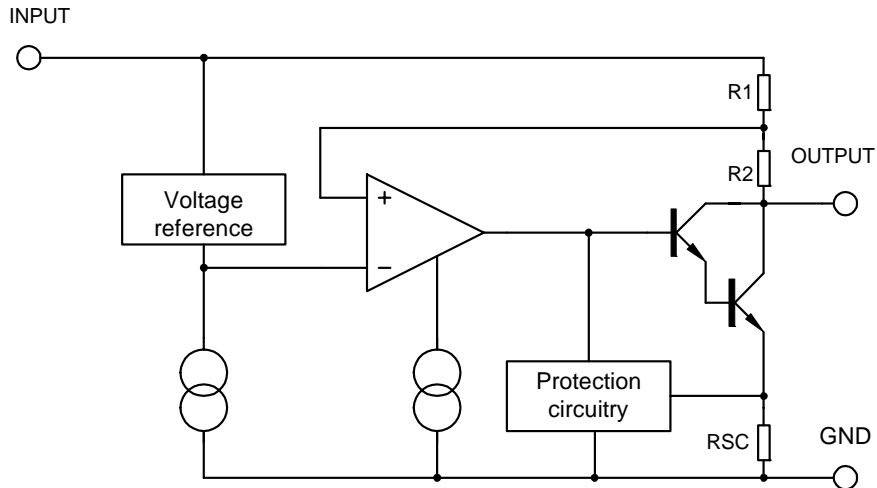
The UTC79XX series of three-terminal negative regulators is available in TO-220 package and with several fixed output voltage, making them useful in a wide range of application. Each type employs internal current limiting, thermal shut-down and safe area protection, making it essentially indestructible. If adequate heat sinking is provided, they can deliver over 1.5A output current. Although designed as fixed voltage regulators, these devices can be used with external components to obtain adjustable voltage and currents.



FEATURES

- *Output current up to 1.5A
- *-5V;-6V;-8V;9V;10V;-12V;-15V;-18V;-24V output voltage available
- *Thermal overload protection
- *Short circuit protection
- *Output transistor SOA protection

BLOCK DIAGRAM



UTC79XX

LINEAR INTEGRATED CIRCUIT

ABSOLUTE MAXIMUM RATINGS (T_a=25°C)

Characteristic	Symbol	Value	Unit	
Input voltage	for V _o =5V to 18V	V _i	-35	V
	for V _o =24V	V _i	-40	V
Thermal resistance junction-air	R _{θJA}	65	°C/W	
Thermal resistance junction-cases	R _{θJC}	5	°C/W	
Operating Temperature	T _{opr}	0~ +125	°C	
Storage Temperature	T _{stg}	-65 ~ +150	°C	

UTC7905 ELECTRICAL CHARACTERISTICS

(Refer to test circuits, 0<T_j<125°C, I_o=500mA, V_i=-10V, C_i=2.2μF, C_o=1μF, unless otherwise specified)

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Units
Output voltage	V _o	T _j =25°C	-4.8	-5.0	-5.2	V
		5.0mA<I _o <1.0A, P _o <15W V _i =-7V to -20V	-4.75	-5.00	-5.25	V
Line regulation	ΔV _o *	T _j =25°C, V _i =-7V to -25V		10	100	mV
		T _j =25°C, V _i =-8V to -12V		5	50	mV
Load regulation	ΔV _o *	T _j =25°C, I _o =5.0mA to 1.5A		10	100	mV
		T _j =25°C, I _o =250mA to 750mA		3	50	mV
Quiescent current	I _q	T _j =25°C		5	8	mA
Quiescent current change	ΔI _q	I _o =5mA to 1.0A			0.5	mA
		V _i =-8V to -25V			0.8	mA
Output voltage drift	ΔV _o /ΔT	I _o =5mA		-0.4		mV/°C
Output noise voltage	V _N	f=10Hz to 100kHz, T _a =25°C		100		μV
Ripple rejection	RR	f=120Hz, V _i =-8V to -18V	54	60		dB
Dropout voltage	V _D	I _o =1.0A, T _j =25°C, ΔV _o =100mV		1.4		V
Short circuit current	I _{sc}	V _i =-35V, T _a =25°C		10		mA
peak current	I _{pk}	T _j =25°C		2.2		A

* load and line regulation are specified at constant junction temperature. Changes in V_o due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

UTC7906 ELECTRICAL CHARACTERISTICS

(Refer to test circuits, 0<T_j<125°C, I_o=500mA, V_i=-11V, C_i=2.2μF, C_o=1μF, unless otherwise specified)

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Units
Output voltage	V _o	T _j =25°C	-5.75	-6.00	-6.25	V
		5.0mA<I _o <1.0A, P _o <15W V _i =-8V to -21V	-5.7	-6.0	-6.3	V
Line regulation	ΔV _o *	T _j =25°C, V _i =-8V to -25V		10	120	mV
		T _j =25°C, V _i =-9V to -13V		5	60	mV
Load regulation	ΔV _o *	T _j =25°C, I _o =5.0mA to 1.5A		10	120	mV
		T _j =25°C, I _o =250mA to 750mA		3	60	mV
Quiescent current	I _q	T _j =25°C		5	8	mA
Quiescent current change	ΔI _q	I _o =5mA to 1.0A			0.5	mA
		V _i =-9V to -25V			0.8	mA
Output voltage drift	ΔV _o /ΔT	I _o =5mA		-0.6		mV/°C
Output noise voltage	V _N	f=10Hz to 100kHz, T _a =25°C		144		μV
Ripple rejection	RR	f=120Hz, V _i =-9V to -19V	54	60		dB
Dropout voltage	V _D	I _o =1.0A, T _j =25°C, ΔV _o =100mV		1.4		V
Short circuit current	I _{sc}	V _i =-35V, T _a =25°C		10		mA
peak current	I _{pk}	T _j =25°C		2.2		A



UTC79XX

LINEAR INTEGRATED CIRCUIT

* load and line regulation are specified at constant junction temperature.Changes in Vo due to heating effects must be taken into account separately.Pulse testing with low duty cycle is used.

UTC7908 ELECTRICAL CHARACTERISTICS

(Refer to test circuits, $0 < T_j < 125^\circ\text{C}$, $I_o = 500\text{mA}$, $V_i = -14\text{V}$, $C_i = 2.2\mu\text{F}$, $C_o = 1\mu\text{F}$, unless otherwise specified)

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Units
Output voltage	Vo	$T_j = 25^\circ\text{C}$	-7.7	-8.0	-8.3	V
		$5.0\text{mA} < I_o < 1.0\text{A}$, $P_o < 15\text{W}$ $V_i = -10.5\text{V}$ to -23V	-7.6	-8.0	-8.4	V
Line regulation	ΔV_o^*	$T_j = 25^\circ\text{C}$, $V_i = -10.5\text{V}$ to -25V		12	160	mV
		$T_j = 25^\circ\text{C}$, $V_i = -11.5\text{V}$ to -17V		5	80	mV
Load regulation	ΔV_o^*	$T_j = 25^\circ\text{C}$, $I_o = 5.0\text{mA}$ to 1.5A		12	160	mV
		$T_j = 25^\circ\text{C}$, $I_o = 250\text{mA}$ to 750mA		4	80	mV
Quiescent current	I_Q	$T_j = 25^\circ\text{C}$		5	8	mA
Quiescent current change	ΔI_Q	$I_o = 5\text{mA}$ to 1.0A			0.5	mA
		$V_i = -11\text{V}$ to -25V			0.8	mA
Output voltage drift	$\Delta V_o / \Delta T$	$I_o = 5\text{mA}$		-0.6		mV/ $^\circ\text{C}$
Output noise voltage	V_N	$f = 10\text{Hz}$ to 100kHz , $T_a = 25^\circ\text{C}$		175		μV
Ripple rejection	RR	$f = 120\text{Hz}$, $V_i = -11.5\text{V}$ to -21.5V	54	60		dB
Dropout voltage	V_D	$I_o = 1.0\text{A}$, $T_j = 25^\circ\text{C}$, $\Delta V_o = 100\text{mV}$		1.4		V
Short circuit current	Isc	$V_i = -35\text{V}$, $T_a = 25^\circ\text{C}$		10		mA
peak current	Ipk	$T_j = 25^\circ\text{C}$		2.2		A

* load and line regulation are specified at constant junction temperature.Changes in Vo due to heating effects must be taken into account separately.Pulse testing with low duty cycle is used.

UTC7909 ELECTRICAL CHARACTERISTICS

(Refer to test circuits, $0 < T_j < 125^\circ\text{C}$, $I_o = 500\text{mA}$, $V_i = -15\text{V}$, $C_i = 2.2\mu\text{F}$, $C_o = 1\mu\text{F}$, unless otherwise specified)

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Units
Output voltage	Vo	$T_j = 25^\circ\text{C}$	-8.6	-9.0	-9.4	V
		$5.0\text{mA} < I_o < 1.0\text{A}$, $P_o < 15\text{W}$ $V_i = -11.5\text{V}$ to -24V	-8.55	-9.0	-9.45	V
Line regulation	ΔV_o^*	$T_j = 25^\circ\text{C}$, $V_i = -11.5\text{V}$ to -26V		12	200	mV
		$T_j = 25^\circ\text{C}$, $V_i = -12.5\text{V}$ to -18V		5	100	mV
Load regulation	ΔV_o^*	$T_j = 25^\circ\text{C}$, $I_o = 5.0\text{mA}$ to 1.5A		12	200	mV
		$T_j = 25^\circ\text{C}$, $I_o = 250\text{mA}$ to 750mA		4	100	mV
Quiescent current	I_Q	$T_j = 25^\circ\text{C}$		5	8	mA
Quiescent current change	ΔI_Q	$I_o = 5\text{mA}$ to 1.0A			0.5	mA
		$V_i = -12\text{V}$ to -26V			0.8	mA
Output voltage drift	$\Delta V_o / \Delta T$	$I_o = 5\text{mA}$		-0.7		mV/ $^\circ\text{C}$
Output noise voltage	V_N	$f = 10\text{Hz}$ to 100kHz , $T_a = 25^\circ\text{C}$		185		μV
Ripple rejection	RR	$f = 120\text{Hz}$, $V_i = -12\text{V}$ to -22V	54	60		dB
Dropout voltage	V_D	$I_o = 1.0\text{A}$, $T_j = 25^\circ\text{C}$, $\Delta V_o = 100\text{mV}$		1.4		V
Short circuit current	Isc	$V_i = -35\text{V}$, $T_a = 25^\circ\text{C}$		10		mA
peak current	Ipk	$T_j = 25^\circ\text{C}$		2.2		A

* load and line regulation are specified at constant junction temperature.Changes in Vo due to heating effects must be taken into account separately.Pulse testing with low duty cycle is used.



UTC79XX

LINEAR INTEGRATED CIRCUIT

UTC7912 ELECTRICAL CHARACTERISTICS

(Refer to test circuits, $0 < T_j < 125^\circ\text{C}$, $I_o = 500\text{mA}$, $V_i = -18\text{V}$, $C_i = 2.2\mu\text{F}$, $C_o = 1\mu\text{F}$, unless otherwise specified)

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Units
Output voltage	V_o	$T_j = 25^\circ\text{C}$	-11.5	-12.0	-12.5	V
		$5.0\text{mA} < I_o < 1.0\text{A}$, $P_o < 15\text{W}$ $V_i = -14.5\text{V}$ to -27V	-11.4	-12	-12.6	V
Line regulation	ΔV_o^*	$T_j = 25^\circ\text{C}$, $V_i = -14.5\text{V}$ to -30V		12	240	mV
		$T_j = 25^\circ\text{C}$, $V_i = -16\text{V}$ to -22V		6	120	mV
Load regulation	ΔV_o^*	$T_j = 25^\circ\text{C}$, $I_o = 5.0\text{mA}$ to 1.5A		12	240	mV
		$T_j = 25^\circ\text{C}$, $I_o = 250\text{mA}$ to 750mA		4	120	mV
Quiescent current	I_Q	$T_j = 25^\circ\text{C}$		5	8	mA
Quiescent current change	ΔI_Q	$I_o = 5\text{mA}$ to 1.0A			0.5	mA
		$V_i = -15\text{V}$ to -30V			0.8	mA
Output voltage drift	$\Delta V_o/\Delta T$	$I_o = 5\text{mA}$		-0.8		mV/ $^\circ\text{C}$
Output noise voltage	V_N	$f = 10\text{Hz}$ to 100kHz , $T_a = 25^\circ\text{C}$		200		μV
Ripple rejection	RR	$f = 120\text{Hz}$, $V_i = -15\text{V}$ to -25V	54	60		dB
Dropout voltage	V_D	$I_o = 1.0\text{A}$, $T_j = 25^\circ\text{C}$, $\Delta V_o = 100\text{mV}$		1.4		V
Short circuit current	I_{sc}	$V_i = -35\text{V}$, $T_a = 25^\circ\text{C}$		10		mA
peak current	I_{pk}	$T_j = 25^\circ\text{C}$		2.2		A

* load and line regulation are specified at constant junction temperature. Changes in V_o due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

UTC7915 ELECTRICAL CHARACTERISTICS

(Refer to test circuits, $0 < T_j < 125^\circ\text{C}$, $I_o = 500\text{mA}$, $V_i = -23\text{V}$, $C_i = 2.2\mu\text{F}$, $C_o = 1\mu\text{F}$, unless otherwise specified)

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Units
Output voltage	V_o	$T_j = 25^\circ\text{C}$	-14.4	-15.0	-15.6	V
		$5.0\text{mA} < I_o < 1.0\text{A}$, $P_o < 15\text{W}$ $V_i = -17.5\text{V}$ to -30V	-14.25	-15	-15.75	V
Line regulation	ΔV_o^*	$T_j = 25^\circ\text{C}$, $V_i = -17.5\text{V}$ to -30V		12	300	mV
		$T_j = 25^\circ\text{C}$, $V_i = -20\text{V}$ to -26V		6	150	mV
Load regulation	ΔV_o^*	$T_j = 25^\circ\text{C}$, $I_o = 5.0\text{mA}$ to 1.5A		12	300	mV
		$T_j = 25^\circ\text{C}$, $I_o = 250\text{mA}$ to 750mA		4	150	mV
Quiescent current	I_Q	$T_j = 25^\circ\text{C}$		5	8	mA
Quiescent current change	ΔI_Q	$I_o = 5\text{mA}$ to 1.0A			0.5	mA
		$V_i = -18\text{V}$ to -30V			0.8	mA
Output voltage drift	$\Delta V_o/\Delta T$	$I_o = 5\text{mA}$		-0.9		mV/ $^\circ\text{C}$
Output noise voltage	V_N	$f = 10\text{Hz}$ to 100kHz , $T_a = 25^\circ\text{C}$		250		μV
Ripple rejection	RR	$f = 120\text{Hz}$, $V_i = -18.5\text{V}$ to -28.5V	54	60		dB
Dropout voltage	V_D	$I_o = 1.0\text{A}$, $T_j = 25^\circ\text{C}$, $\Delta V_o = 100\text{mV}$		1.4		V
Short circuit current	I_{sc}	$V_i = -35\text{V}$, $T_a = 25^\circ\text{C}$		10		mA
peak current	I_{pk}	$T_j = 25^\circ\text{C}$		2.2		A

* load and line regulation are specified at constant junction temperature. Changes in V_o due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.



UTC79XX

LINEAR INTEGRATED CIRCUIT

UTC7918 ELECTRICAL CHARACTERISTICS

(Refer to test circuits, $0 < T_j < 125^\circ\text{C}$, $I_o = 500\text{mA}$, $V_i = -27\text{V}$, $C_i = 2.2\mu\text{F}$, $C_o = 1\mu\text{F}$, unless otherwise specified)

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Units
Output voltage	V_o	$T_j = 25^\circ\text{C}$	-17.3	-18.0	-18.7	V
		$5.0\text{mA} < I_o < 1.0\text{A}$, $P_o < 15\text{W}$ $V_i = -21\text{V}$ to -33V	-17.1	-18	-18.9	V
Line regulation	ΔV_o^*	$T_j = 25^\circ\text{C}$, $V_i = -21\text{V}$ to -33V		15	360	mV
		$T_j = 25^\circ\text{C}$, $V_i = -24\text{V}$ to -30V		8	180	mV
Load regulation	ΔV_o^*	$T_j = 25^\circ\text{C}$, $I_o = 5.0\text{mA}$ to 1.5A		15	360	mV
		$T_j = 25^\circ\text{C}$, $I_o = 250\text{mA}$ to 750mA		5	8	mV
Quiescent current	I_Q	$T_j = 25^\circ\text{C}$		5	8	mA
Quiescent current change	ΔI_Q	$I_o = 5\text{mA}$ to 1.0A			0.5	mA
		$V_i = -21\text{V}$ to -32V			0.8	mA
Output voltage drift	$\Delta V_o / \Delta T$	$I_o = 5\text{mA}$		-1		mV/ $^\circ\text{C}$
Output noise voltage	V_N	$f = 10\text{Hz}$ to 100kHz , $T_a = 25^\circ\text{C}$		300		μV
Ripple rejection	RR	$f = 120\text{Hz}$, $V_i = -22\text{V}$ to -32V	54	60		dB
Dropout voltage	V_D	$I_o = 1.0\text{A}$, $T_j = 25^\circ\text{C}$, $\Delta V_o = 100\text{mV}$		1.4		V
Short circuit current	I_{sc}	$V_i = -35\text{V}$, $T_a = 25^\circ\text{C}$		10		mA
peak current	I_{pk}	$T_j = 25^\circ\text{C}$		2.2		A

* load and line regulation are specified at constant junction temperature. Changes in V_o due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

UTC7924 ELECTRICAL CHARACTERISTICS

(Refer to test circuits, $0 < T_j < 125^\circ\text{C}$, $I_o = 500\text{mA}$, $V_i = -33\text{V}$, $C_i = 2.2\mu\text{F}$, $C_o = 1\mu\text{F}$, unless otherwise specified)

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Units
Output voltage	V_o	$T_j = 25^\circ\text{C}$	-23	-24	-25	V
		$5.0\text{mA} < I_o < 1.0\text{A}$, $P_o < 15\text{W}$ $V_i = -27\text{V}$ to -38V	-22.8	-24	-25.2	V
Line regulation	ΔV_o^*	$T_j = 25^\circ\text{C}$, $V_i = -27\text{V}$ to -38V		15	480	mV
		$T_j = 25^\circ\text{C}$, $V_i = -30\text{V}$ to -36V		8	240	mV
Load regulation	ΔV_o^*	$T_j = 25^\circ\text{C}$, $I_o = 5.0\text{mA}$ to 1.5A		15	480	mV
		$T_j = 25^\circ\text{C}$, $I_o = 250\text{mA}$ to 750mA		5	240	mV
Quiescent current	I_Q	$T_j = 25^\circ\text{C}$		5	8	mA
Quiescent current change	ΔI_Q	$I_o = 5\text{mA}$ to 1.0A			0.5	mA
		$V_i = -27\text{V}$ to -38V			0.8	mA
Output voltage drift	$\Delta V_o / \Delta T$	$I_o = 5\text{mA}$		-1		mV/ $^\circ\text{C}$
Output noise voltage	V_N	$f = 10\text{Hz}$ to 100kHz , $T_a = 25^\circ\text{C}$		400		μV
Ripple rejection	RR	$f = 120\text{Hz}$, $V_i = -28\text{V}$ to -38V	54	60		dB
Dropout voltage	V_D	$I_o = 1.0\text{A}$, $T_j = 25^\circ\text{C}$, $\Delta V_o = 100\text{mV}$		1.4		V
Short circuit current	I_{sc}	$V_i = -35\text{V}$, $T_a = 25^\circ\text{C}$		10		mA
peak current	I_{pk}	$T_j = 25^\circ\text{C}$		2.2		A

* load and line regulation are specified at constant junction temperature. Changes in V_o due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.



UTC79XX LINEAR INTEGRATED CIRCUIT

APPLICATION CIRCUITS

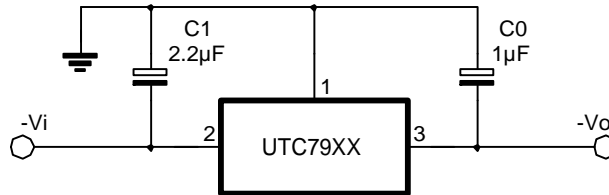


Fig.2 Fixed output regulator

NOTE:

1. To specify an output voltage ,substitute voltage value for'xx'.
2. Required for stability.For value given,capacitor must be solid tantalum.If aluminium electrolytics are used,at least ten times value should be selected.C1 is required if regulator is located an appreciable distance from power supply filter.
3. To improve transient response.If large capacitors are used,a high current diode from input to output(1N4001 or similar)should be introduced to protect the device from momentary input short circuit.

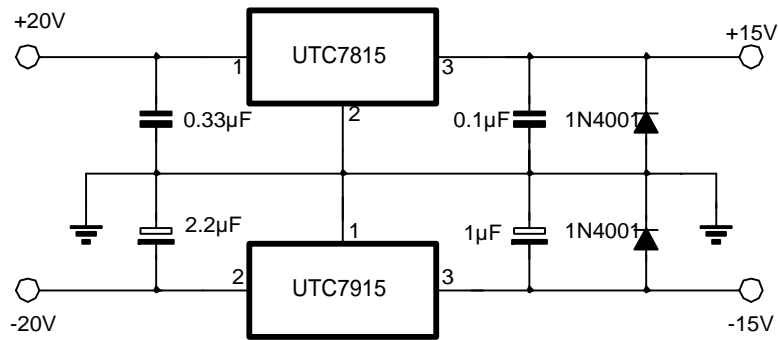


Fig.3 Split power supply($\pm 15V, 1A$)

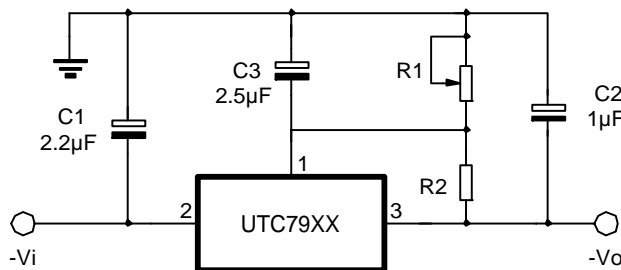


Fig.4 Circuit for increasing output voltage